



05/31/00

UTILITY PATENT APPLICATION TRANSMITTAL UNDER 37 C.F.R. §1.53(b)

jc586 U.S. PTO
09/584096
05/31/00

ASSISTANT COMMISSIONER FOR PATENTS
Box PATENT APPLICATION
Washington D.C. 20231

Case Docket No.: CIT/K-018A

Sir:

Transmitted herewith for filing is the patent application of
INVENTOR OR APPLICATION IDENTIFIER: Sung Tae KIM

FOR: ORGANIC ELECTROLUMINESCENT DEVICE AND METHOD FOR FABRICATING THE SAME

Enclosed are:

1. [X] 11 pages of specification, claims, abstract
2. [X] 5 sheets of FORMAL drawings.
3. [X] 2 pages of Declaration & Power of Attorney (copy from a prior application).

[X] Incorporation By Reference-The entire disclosure of the prior application is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

[X] This is a [] Continuation or [X] Divisional of prior application No. 09/058,293, filed on April 10, 1998, entitled **ORGANIC ELECTROLUMINESCENT DEVICE AND METHOD FOR FABRICATING THE SAME**
4. [X] An Information Disclosure Statement and PTO 1449 Form are submitted herewith.
5. [X] Cancel claims 4-10.
6. The filing fee is calculated on the basis of the claims existing in the prior application as amended at 5 above and the Preliminary Amendment:

CLAIMS AS FILED					
For	No. Filed		No. Extra	Rate	Fee
Total Claims	7	- 20	0	X \$18.00	0
Indep. Claims	2	- 3	0	X \$78.00	0
Multiple Dependent Claims (If applicable) add \$260.					
BASIC FEE					\$690.00
TOTAL FILING FEE					\$690.00

7. [X] Preliminary Amendment is enclosed.
- 8a. [] Enclosed is a Small Entity Statement.
- 8b. [] A Small Entity Statement was filed in prior application and such status is still proper and desired.
- 9a. [X] A check in the amount of \$690.00 (Check # 8782) is attached.
- 9b. [] Please charge my Deposit Account No. 16-0607 in the amount of \$___. A duplicate copy of this sheet is enclosed.
- 9c. [X] The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 16-0607. A duplicate copy is enclosed.
- [] Any additional filing fees required under 37 C.F.R. 1.16.

10a. [X] The Commissioner is hereby authorized to charge fees under 37 C.F.R. 1.16 and 1.17 which may be required, including any extension of time fees to maintain the pendency of the parent application No. 09/058,293 or credit any overpayment to Deposit Account No. 16-0607.

10b. [X] Authorization under 37 C.F.R. §1.136(a)(3).

11a. [] Amend the specification by inserting before the first line the sentence:

-This application is a Continuation of Application No. filed.-

11b. [X] Amend the specification by inserting before the first line the sentence:

-This application is a Divisional of Application No. 09/058,293 filed April 10, 1998.-

12. [X] Priority of Application Serial No. 47579/1997 filed on September 18, 1997 in Republic of Korea is claimed under 35 U.S.C. 119. The certified priority document(s) were filed in Serial No. 09/058,293 on April 10, 1998.

13. [X] The prior application is assigned of record to LG ELECTRONICS INC. at Reel 9116, Frame 0452

14. [X] The power of attorney in the prior application is to:

FLESHNER & KIM, LLP

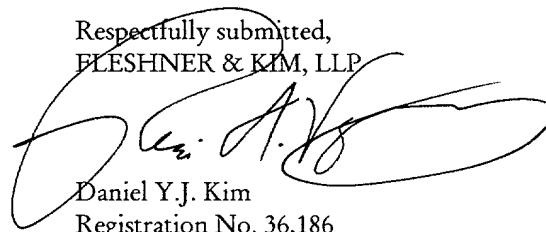
15. [X] Two (2) return postcards.

[X] Stamp & Return with Courier.

[X] Prepaid Postcard-Stamped Filing Date & Returned with Unofficial Serial Number.

16. [X] Also enclosed: Preliminary Amendment

17. [] A petition, fee and response has been filed to extend the term in the pending prior application until _____.

Respectfully submitted,
FLESHNER & KIM, LLP

Daniel Y.J. Kim
Registration No. 36,186
René A. Vazquez
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Date: May 31, 2000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Sung Tae KIM :
Divisional of Application Serial No. 09/058,293 :
Filed: May 31, 2000 :
For: ORGANIC ELECTROLUMINESCENT DEVICE AND METHOD
FOR FABRICATING THE SAME

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

Prior to initial examination on the merits, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please amend the specification as follows:

Page 6, line 2, please replace "Figs. 1A ~ 1D" with --Figures 1a, 1b, 1c and 1d--.

line 4, please replace "Figs. 2A ~ 2E" with --Figures 2a, 2b, 2c, 2d and 2e--.

line 11, please replace "in" with --at--.

Page 7, line 21, please replace "Dr. Blade" with --doctor blade--.

Page 8, lines 17-19, please delete the last sentence.

IN THE CLAIMS:

Please amend claims 1 and 3 as follows:

1. (Amended) An electroluminescent device having a plurality of pixels each with a first electrode stripe, an organic electroluminescent multilayer and a second electrode stripe, the device comprising:

5 a partition wall of an electrically insulating material projected from each of the first electrode stripes, the partition wall having a trapezoidal structure with [the lower side wider than the upper side] a base closer to the substrate wider than a base away from the substrate, wherein portions of the second electrode and [possibly] the organic luminescence multilayer formed on the partition wall are etched out, thereby any two adjacent pixels being electrically isolated.

3. (Amended) A device as claimed in claim 1, further comprising [the] a first protective film formed only on each of the emitting pixels and [the] a second protective film formed on the entire surface including on top of the first protective film.

Please add new claims 11-14 as follows:

--11. An electroluminescent device, comprising:

a first electrode layer having a plurality of electrode stripes positioned on a substrate;

a plurality of pixels formed on the first electrode layer, and

5 an array of partition walls, formed directly above and on the first electrode layer, and made of an electrically insulating material, said partition walls having a base closer to the substrate wider than a base further from the substrate;

wherein each partition wall in the array of partition walls is positioned between at least two of the plurality of pixels, thereby electrically isolating the at least two pixels from each other.--

--12. The electroluminescent device of claim 11, further comprising a second protection layer on a surface of said plurality of electrically isolated pixels.--

--13. The electroluminescent device of claim 11, wherein the partition walls are formed of one of photo resist, silicon nitride and silicon oxide.--

--14. The electroluminescent device of claim 11, each of said plurality of pixels comprising:

an organic electroluminescent layer having a first surface in contact with side surfaces of the partition walls and the first electrode layer;

a second electrode layer having a first surface in contact with a second surface of the organic electroluminescent layer; and

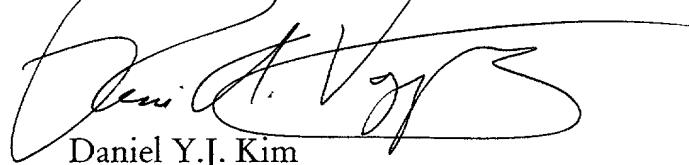
a first protection layer having a first surface in contact with a second surface of the second electrode layer,

wherein each of the plurality of pixels is positioned between at least two partition walls.--

REMARKS

Claims 1-3 and 11-14 are pending. Prompt examination and allowance in due course are respectfully solicited.

Respectfully submitted,
FLESHNER & KIM, LLP



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Date: May 31, 2000

ORGANIC ELECTROLUMINESCENT DEVICE AND METHOD FOR FABRICATING THE SAME

BACKGROUND OF THE INVENTION

Field of the Invention

5 The present invention relates to a display, and more particularly, to an organic electroluminescent (EL) device and a method for fabricating the same.

Discussion of the Related Art

10 Organic EL devices, also called organic light emitting diodes (LEDs), are becoming very popular because of their possible application to flat panel displays (FPDs). They are extremely thin, matrix-addressable and can be operated at a relatively low voltage, typically less than 15 volts. Furthermore, they have additional characteristics suitable for next generation FPDs such as little dependence on viewing angle and good device-formability on flexible substrates among other things. Organic LEDs differ fundamentally from conventional inorganic LEDs. While the 15 charge transfer in inorganics is band-like in nature and the electron-hole recombination results in the interband emission of light, organic films are generally characterized by the low-mobility activated hopping transport and the emission is excitonic. Organic EL devices are also substantially different from conventional inorganic EL devices, especially in that organic EL devices can be operated at low DC voltages.

20 A substantial amount of research has been directed toward the efficiency improvement and color control of organic LEDs. The efficiency of some organic EL devices has now been demonstrated to be close to its theoretical limit and certainly adequate for many commercial

applications. Moreover, the color control is probably not limiting for many potential applications.

In light of this, we believe that the outlook for commercial applications is excellent for organic EL devices. Their performance is quite satisfactory for many applications. It is valuable to think 5 in terms of specific products and manufacturing techniques for the commercialization of organic EL devices. Consideration of the specific applications leads us to believe that more work on manufacturability, uniformity, reliability, and systems issues is required to commercialize organic EL devices.

Pixelation or patterning, especially of electroluminescent and second electrode materials, 10 is one of the key issues to be solved before the commercialization of organic EL devices. The use of many conventional pixelation techniques is precluded due to the nature of organic materials which are extremely vulnerable to the attack by most solvents. Various efforts have been made to utilize shadowing effects for the film patterning. One approach was introduced by C. Tang et al. in U.S. Pat. No. 5,294,870, wherein partition walls were used as a means to give a shadowing 15 effect. Therein each stripe of the second electrode is supposed to be separated by depositing the second electrode material onto the transparent support with relatively high partition walls at an oblique angle with the surface normal. The idea may be conceivable, but is not suitable at all for mass production. First of all, it is impossible to have a vapor source which may set the direction 20 of traveling metallic vapor at a constant angle with the surface normal over the whole panel surface. Whether one has either a point source or a large area source, the angle would vary with the position of pixel on the transparent support because of the diverging nature of vapor. One may be able to obtain a certain degree of separation with a complicate arrangement of source and support, but only at the sacrifice of production yield. More practical processes have been

proposed by K. Nagayama et al. in U.S. Pat. No. 5,701,055 wherein electrically insulating ramparts, like a bunch of tiny umbrellas, serve as a means to shadow the incoming vapor. An advantage of U.S. Pat. No. 5,701,055 over U.S. Pat. No. 5,294,870 is that depositions do not need to be made at an oblique angle, which markedly widens the process window.

5 Figs. 1a ~ 1d illustrate typical process steps proposed in U.S. Pat. No. 5,701,055. Referring to Fig. 1a, an array of anode stripes 2 is formed of typically indium tin oxide on a transparent substrate 1, and insulating patterns 3 are formed thereon. As shown in Fig. 1b, next, each of ramparts 4 is formed to fit into the small gap between a pair of the insulating patterns 3. The rampart 4 with a pair of overhangs 8 serves an important role to shadow the incoming vapor.

10 When organic materials and a second electrode material are vapor deposited, the films are formed on top of the ramparts as well as on the surface between the ramparts, as seen in Fig. 1c. The important point is that the films are not formed on the side surface of the ramparts due to a shadowing effect, which ensures the electrical insulation between any two adjacent pixels. After the formation of an electroluminescent multilayer 5 and a second electrode 6 on the first electrode

15 2 in succession, an encapsulation layer 7 is formed, as in Fig. 1d. In summary, the background art of U.S. Pat. No. 5,701,055 appears to be an effective way to pixellate an organic electroluminescent display panel.

However, the background art EL device has several drawbacks as follows.

(1) While a variety of ramparts with the different shape is shown in U.S. Pat. No. 20 5,701,055, the most typical one looks like that shown in Fig. 1b. Since this type of rampart 4 easily causes short-circuits between the first and second electrodes at the edges of the second electrode stripe, it is often necessary to form additional insulating patterns 3. But it is not necessary to fabricate them in the present invention.

(2) Ramparts are often formed of photoresist and the most important parts of them are the overhangs 8 which actually shadow the incoming vapor. It is quite a delicate process to make ramparts with good overhangs. The present invention provides ramparts, or partition walls, the production of which can be much simplified.

5 (3) Ramparts remain standing on the panel surface even after the completion of the device fabrication. A part of rampart itself or films on top of them may fall down to damage the device locally during the subsequent fabrication processes. To avoid this kind of potential problems, it is recommended to have a stable structure: the shape of trapezoid in the present invention is structurally more stable compared to the inverted trapezoid of the related art.

10 SUMMARY OF THE INVENTION

Accordingly, the present invention is directed to an organic electroluminescent device and a method for fabricating the same that substantially obviates one or more of the problems due to the limitations and disadvantages of the related art.

15 An object of the present invention is to provide an organic electroluminescent device and a method for fabricating the same, in which adequate pixellation can be achieved using simple and stable partition walls.

Another object of this invention lies in that the stable and solid feature of the partition walls employed in the invention can make a contribution towards the enhanced long-term stability of the electroluminescent device fabricated thereby.

20 Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as

well as the appended drawings.

To achieve these and other advantages in accordance with the purpose of the present invention, as embodied and broadly described, the electroluminescent device includes a plurality of pixels each with a first electrode, an organic luminescence multilayer, a second electrode, and 5 successively prepared protection layers. For the purpose of pixellation, the device also has an array of partition walls with a trapezoidal structure made of an electrically insulating material. To achieve the electrical isolation between two adjacent pixels, the panel surface with a multilayer structure is subjected to a dry etching process.

In the other aspect of this invention, there is provided a method for fabricating an 10 electroluminescent device, including the steps of (1) forming a plurality of first electrode stripes at fixed intervals on a transparent substrate, (2) forming an array of partition walls with a trapezoidal structure made of an electrically insulating material, (3) laminating an organic electroluminescent multilayer and a second electrode layer in succession on the entire surface including on top of the partition walls, and (4) subjecting the panel surface to a dry etching 15 process in order to remove a part of the films, especially the second electrode layer formed on top of the partition walls, thereby electrically isolating two adjacent pixels.

It is to be understood that both the foregoing general description and the following 20 detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are included to provide further understanding of the invention and constitute a part of this specification, illustrate embodiments of the invention and together with the descriptions serve to explain the principles of the invention:

In the drawings:

Figs. 1a ~ 1d illustrate the fabrication steps of an organic electroluminescent device in accordance with a typical embodiment of the background art; and,

5 Figs. 2a ~ 2e illustrate the fabrication steps of an organic electroluminescent device in accordance with a preferred embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Reference will now be made in detail to the preferred embodiments of the present invention, an example of which is illustrated in the accompanying drawings. Figs. 2a ~ 2e illustrate the fabricating steps of an organic electroluminescent device in accordance with a preferred embodiment of the present invention which has basically a simple XY matrix structure with a plurality of first and second electrode stripes intersecting each other in 90 degree.

As shown in Fig. 2a, the fabrication processes begin with the formation of a plurality of the first electrode stripes 12 on a transparent substrate 11. Partition walls 13 may be made of either organic or inorganic materials as long as they are electrically insulating: for example, photoresist, silicon oxide, silicon nitride and silicon carbide among other things. After a film for partition walls 13 is coated on the first electrode elements 12, photolithography and etching processes are applied to pattern it as shown in Fig. 2b. Each partition wall 13 is preferably formed to have a trapezoidal structure with the angle θ being below 90° which is more stable and easy to fabricate compared to the background art. A partition wall 13 may be 500nm ~ 10,000nm high, 15 and preferably 2,000nm high. It may have a width of 100nm ~ 50,000nm though the width is more or less dependent on the display resolution. i.e. the pitch between pixels. Next, the panel may be subjected to a process of vacuum annealing and UV hardening, especially if the partition walls 13 are made of organic materials such as photoresist, whereby the partition walls are 20

hardened and residual solvents are removed as completely as possible. Each rampart of the background art typically has a pair of overhangs which are often more fragile and susceptible to heat than other parts. But the partition walls of the present invention are structurally more stable and less susceptible to heat-treatments, which certainly widens the process window. On top of the transparent support with the partition walls 13, next, an organic electroluminescent multilayer 14, the second electrode layer 15 and the first protection layer 16 are formed in succession, as shown in Fig. 2c. The EL multilayer 14 for green emission typically constitutes of (1) a buffer layer of copper phthalocyanine (CuPc) with a typical thickness of 10nm ~ 20nm, (2) a hole transporting layer of N,N'-diphenyl-N,N'-bis(3-methylphenyl)-(1,1'-biphenyl)-4,4'-diamine (TPD) with a thickness of 30nm ~ 50nm, and (3) an emitting layer of tris(8-hydroxy-quinolate)aluminum (Alq₃) co-deposited with a dopant of coumarin 6. The thickness of Alq₃ layer lies in the range of 40nm ~ 60nm and the dopant concentration is roughly 1.0 wt.%. The second electrode 15 is often made of aluminum, Al:Li or Mg:Ag. The first protection layer 16 serves a role to protect the EL elements from moisture and oxygen, and may be made of one or more materials selected from metals, alloys and organics. It is also necessary for the outer surface of the layer 16 to be reasonably well planarized, as seen in Fig. 2c. The planarization of the first protection layer 16 contributes later to keeping unetched the EL multilayer and the second electrode in the emitting zone between a pair of partition walls while etching out those on top of the partition walls. In order to be moisture-resistant as well as planarized, the protection layer 16 may constitute of a multilayer with a moisture absorbing layer, a moisture resistant layer and a planarizing layer, the last of which is preferably formed by spin-coating or Dr. Blade method.

Fig. 2d shows the step of etching out the upper part of films, unequivocally including the second electrode on top of the partition walls to ensure the electrical isolation between two adjacent

pixels. The etching process is preferably carried out using a dry etching method such as reactive ion etching, and the selection of etching gases and etching parameters such as working pressure and plasma power should be carefully made for the balanced removal of involved materials. Finally, as seen in Fig. 2e, the second protection layer 17 is laminated on top of the etched surface to complete the fabrication processes of the present invention. The second protection layer differs from the first protection layer in that the former does not necessarily need a function of planarization.

The organic electroluminescent device of the present invention and the method for fabricating the same have the following advantages.

10 The use of a stable and simple structure for partition walls instead of those with overhangs used in the background art allows easy fabrication and thereby significant improvement in the production yield.

In addition, the stable and solid feature of the partition walls presumably makes a contribution towards the enhanced long-term stability of the device fabricated thereby.

15 It will be apparent to those skilled in the art that various modifications and variations can be made in the organic electroluminescent device and the method for fabricating the same of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is Claimed is:

1. An electroluminescent device having a plurality of pixels each with a first electrode stripe, an organic electroluminescent multilayer and a second electrode stripe, the device comprising:

5 a partition wall of an electrically insulating material projected from each of the first electrode stripes, the partition wall having a trapezoidal structure with the lower side wider than the upper side, wherein portions of the second electrode and possibly the organic luminescence multilayer formed on the partition wall are etched out, thereby any two adjacent pixels being electrically isolated.

10 2. A device as claimed in claim 1, wherein the partition wall is formed of a material selected from photoresist, silicon nitride and silicon oxide.

3. A device as claimed in claim 1, further comprising the first protective film formed only on each of the emitting pixels and the second protective film formed on the entire surface including on top of the first protective film.

15 4. A method for fabricating an electroluminescent device, comprising the steps of:

(1) forming a plurality of first electrode stripes at fixed intervals on a transparent substrate;

(2) forming an array of partition walls made of an electrically insulating material on the first electrode elements; having a trapezoidal structure with the lower side wider than the upper side;

(3) forming an organic electroluminescent multilayer, the second electrode layer, and the first protection layer in succession on the entire surface including on top of the partition walls;

(4) removing upper portions of films, unequivocally including the second electrode layer on top of the partition walls, whereby electrically isolating any two adjacent pixels; and,

5 (5) forming the second protection layer on top of the etched-out surface.

5. A method as claimed in claim 4, wherein the first protective coating is formed by a method selected from spin-coating and Dr. Blade method.

6. A method as claimed in claim 4, wherein the partition wall is formed of a material selected from photoresist, silicon nitride and silicon oxide.

ABSTRACT

An organic electroluminescent device and a method for fabricating the same are disclosed, the method including the steps of (1) forming a plurality of first electrode stripes on a transparent substrate at fixed intervals, (2) forming an array of partition walls made of an electrically insulating material on the first electrode elements; having a trapezoidal structure with the lower side wider than the upper side, (3) forming an organic electroluminescent multilayer, the second electrode, and the first protection layer in succession on the entire surface including on top of the partition walls, (4) removing upper portions of films, unequivocally including the second electrode layer on top of the partition walls, whereby electrically isolating any two adjacent pixels, and (5) forming the second protection layer on top of the etched-out surface, whereby simplifying fabrication processes, improving product yield and reducing product cost.

FIG.1a
background art

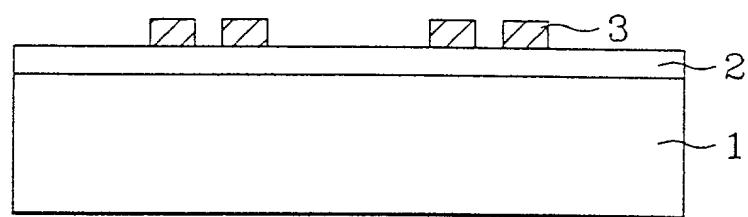


FIG.1b
background art

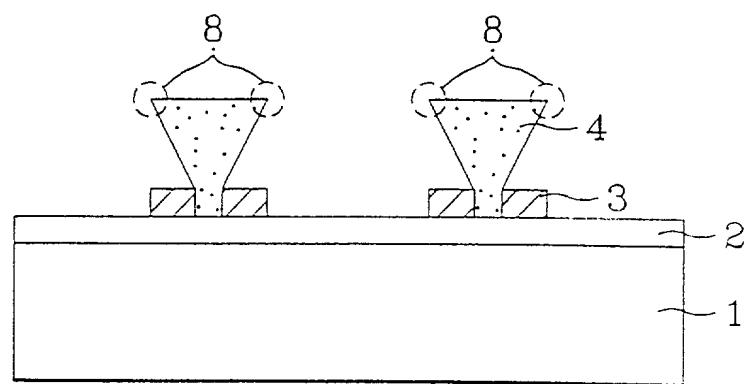


FIG.1c
background art

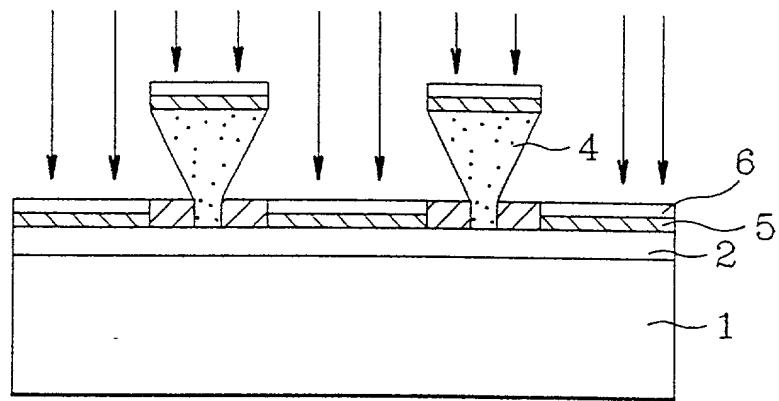


FIG.1d
background art

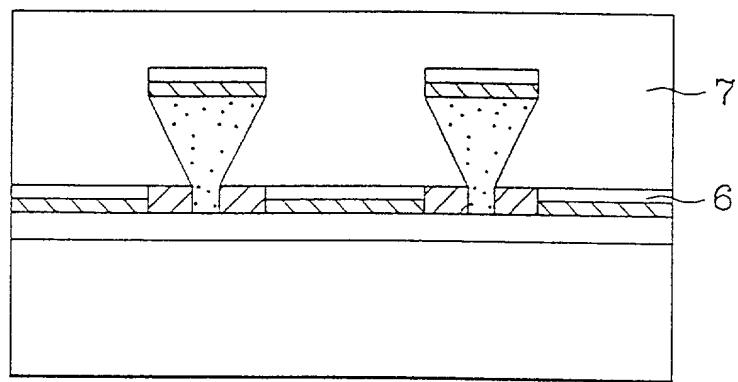


FIG.2a

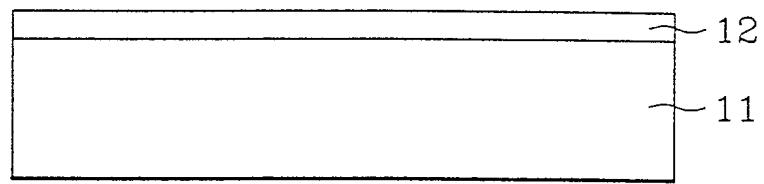


FIG.2b

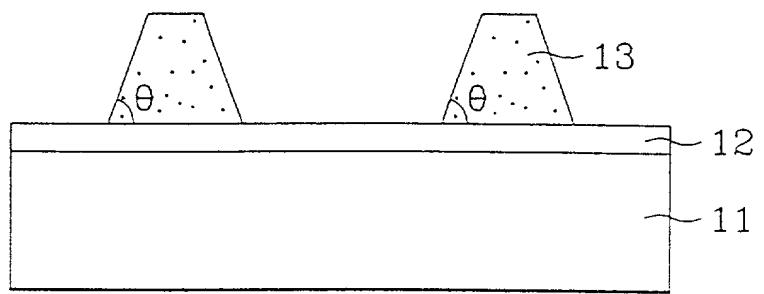


FIG.2c

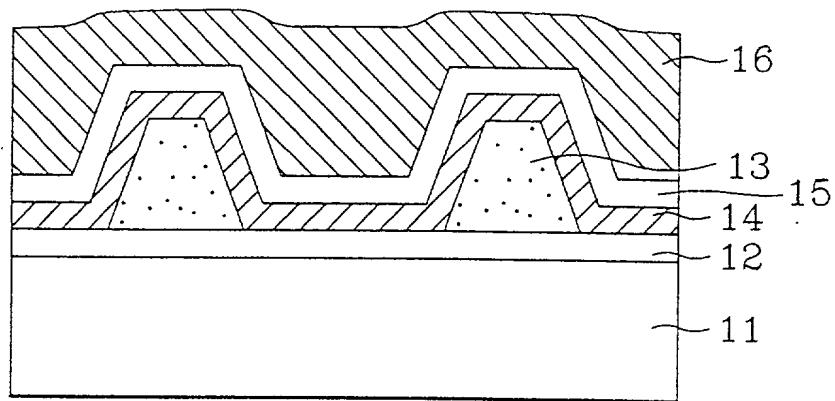


FIG.2d

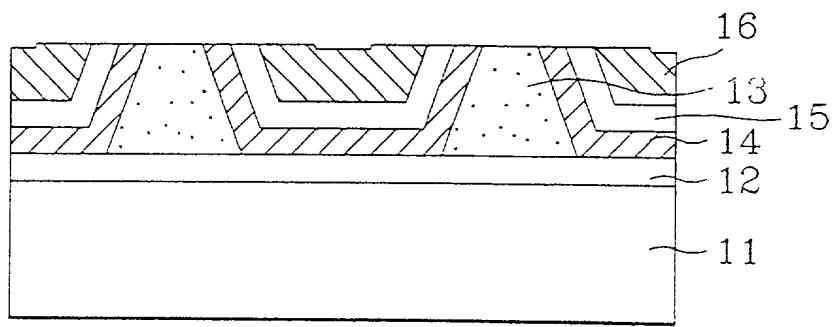
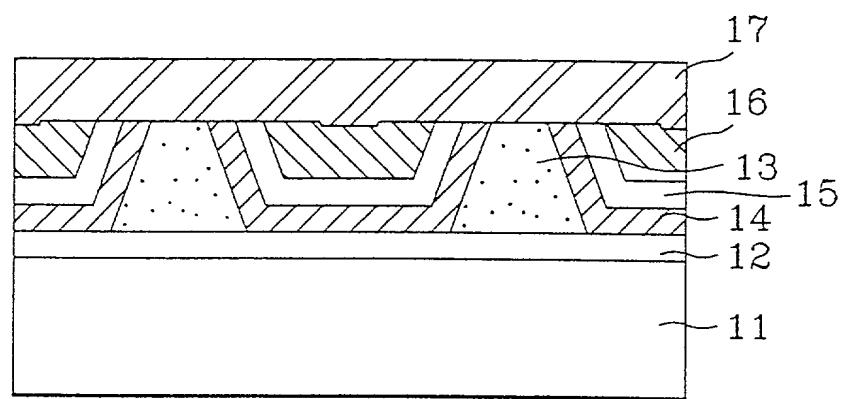


FIG.2e



Docket No.: _____

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought on the invention entitled _____

ORGANIC ELECTROLUMINESCENT DEVICE AND METHOD FOR FABRICATING THE SAME

the specification of which

[X] is attached hereto [] was filed on _____ as Application Serial No. _____ and was amended on _____ (if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is known to me to be material to patentability in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim foreign priority or provisional application benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate, or provisional application(s) listed below and have also identified below any foreign application for patent or inventor's certificate, or provisional application(s) having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s) or U.S. Provisional Application(s):			Priority Claimed	
Number	Country	Day/Month/Year	Yes	No
47579/1997	Korea	18/ Sep/ 1997	X	

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, in so doing, as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U. S. Application(s):	Filing Date	Status: Patented, Pending, Abandoned
Serial No.		

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful statements may jeopardize the validity of the application or any patent issued thereon.

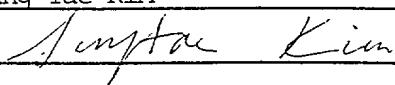
I hereby appoint the following attorney(s) and/or agent(s): Daniel Y.J. Kim, Registration No. 36,186 and Mark L. Fleschner, Registr No. 34,596; Mark G. Toohey, Registration No. 35,392; Carl R. Wesolowski, Registration No. 40,372, John C. Eisenhart, Registr No. 38,128, and Rene A. Vazquez, Registration No. 38,547, all of

The Law Offices of
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with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trade Office connected therewith, and all future correspondence should be addressed to them.

Full name of sole or first inventor: Sung Tae KIM

Inventor's signature:



Date: Mar 23 /

Residence: Seoul, Korea

Citizenship: Republic of Korea

Post Office Address: 242-61, Eungam2-dong, Eunpyung-ku, Seoul, Korea

Full name of joint inventor(s):

Inventor's signature:

Date:

Residence:

Citizenship:

Post Office Address:

Full name of joint inventor(s):

Inventor's signature:

Date:

Residence:

Citizenship:

Post Office Address: